

**Job Title:** FPGA/ASIC Firmware Design Engineer**Location:** Cincinnati, OH**Description:**

GIRD Systems is seeking an FPGA/ASIC Design Engineer for Cincinnati, OH. This person will design and implement waveforms and DSP algorithms using VHDL or Verilog targeting software defined radio (SDR) hardware. Firmware development is applied to high complexity and high throughput implementations with a focus on VHDL, code re-use, fixed point modeling, low power design, system integration, test automation, FPGA/ASIC IP Core development and product test/support while leveraging advancements in SoCs, synthesis tools and SDR hardware. We are looking for individuals who have demonstrated ability in innovative thinking and versatility in their jobs with experience in the following: FPGA/ASIC architecture design, simulation and verification, MATLAB or C/C++ familiarity and proposal, report and system/product specification development. The individual will work within a team environment to achieve high efficiency in solving our customer's formidable challenges and meeting our customer's requirements objectives. The company has been going through steady growth and has been awarded many interesting and challenging projects.

We seek highly motivated, innovative and forward thinking individuals to grow with the company. We provide our employees with competitive compensation packages and a full range of benefits, including vacation/holiday, medical, dental, IRA match and tuition assistance.

**Position:** Contract to hire or full-time employment**Clearance Requirement:** No, but US Citizenship is a prerequisite**Travel Requirements:** This position may require overnight travel up to 10 % of the time**Relocation Assistance:** May be provided/negotiable**Roles and Responsibilities:**

- Demonstrated leadership skills/experience in digital design engineering, HDL coding, implementation of waveforms/modems and digital signal processing, performance/test analysis and technical team leadership
- Implementation of waveforms/algorithms in VHDL (preferred) or Verilog
- Experienced with lab and field testing, test equipment such as signal generators and spectrum and digital logic analyzers
- Familiarity with signal processing systems, terrestrial/airborne/satellite transmitter and receiver design including acquisition, tracking and demodulation, MATLAB, C/C++, synthesis, verification/acceptance testing
- Individuals with a background developing solutions for military/DoD customers is plus
- Provide concise explanation and design documentation of developed firmware implementation to enable team members to support system integration
- Identification, development and implementation of FPGA/ASIC IP Cores
- Deliver effective briefings and to work efficiently in a multidisciplinary team environment
- Travel to support events such as customer site support, training seminar, proposals
- Proactively ensure a safe work environment and adhere to GIRD Systems policies and procedures

**Qualifications:**

Bachelor's degree and minimum 4 years of work experience or Master's degree and minimum of 2 years of work experience in Electrical/Computer Engineering specializing in wireless communications/DSP, system design and new product development. Must be proficient in VHDL or Verilog.

GIRD Systems is committed to hiring and retaining a diverse workforce. We are an equal opportunity employer and all qualified applicants will receive consideration for employment without regard to race, color, age, religion, sex, national origin, disability status, protected veteran status or any other characteristic protected by law.